

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
23 December 2004 (23.12.2004)

PCT

(10) International Publication Number
WO 2004/111857 A2

(51) International Patent Classification⁷: G06F 12/06 (74) Agent: VOLMER, Georg; Philips Intellectual Property & Standards GmbH, Weiss hausstr. 2, 52066 Aachen (DE).

(21) International Application Number: PCT/IB2004/050842 (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(22) International Filing Date: 4 June 2004 (04.06.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data: 03101766.8 17 June 2003 (17.06.2003) EP

(71) Applicant (for DE only): PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH [DE/DE]; Stein-damm 94, 20099 Hamburg (DE).

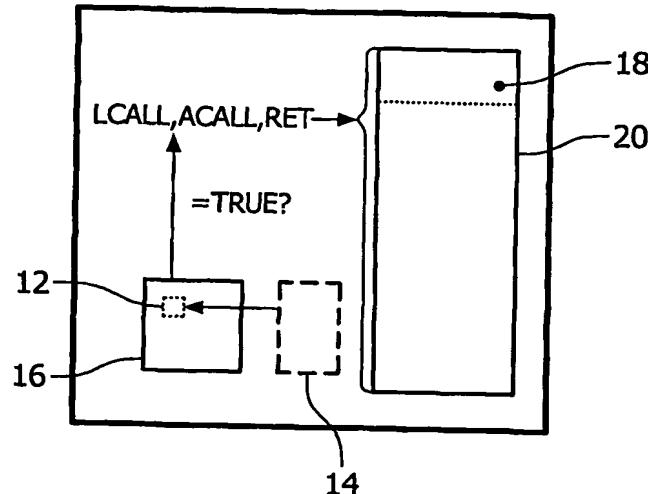
(71) Applicant (for all designated States except DE, US): KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): KRAMER, Torsten [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weiss hausstr. 2, 52066 Aachen (DE). FEUSER, Markus [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weiss hausstr. 2, 52066 Aachen (DE).

(54) Title: MICROCONTROLLER AND ADDRESSING METHOD

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(57) Abstract: In order to provide a microcontroller and an addressing method which are distinguished by a lower storage requirement and a higher execution speed than previously known when addressing N-bit address spaces, the address length N of the N-bit address word being greater than the address length of a standard set of instruction or of equivalents of other sets of instructions of the microcontroller, it is provided that the microcontroller (10) has at least one status bit (12) by means of which a writing and/or reading of N-bit address words by at least one standard instruction of the microcontroller (10) can be forced, and the at least one status bit (12) of a microcontroller (10) is set and as a result a writing and/or reading of N-bit address words by means of at least one standard instruction of the microcontroller (10) is forced.

Published:

— without international search report and to be republished upon receipt of that report

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